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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/671,767	09/27/2000	Scott B. Blum	042390.P9021	7450
8791	7590	04/19/2005	EXAMINER	
BLAKELY SOKOLOFF TAYLOR & ZAFMAN 12400 WILSHIRE BOULEVARD SEVENTH FLOOR LOS ANGELES, CA 90025-1030			LEVITAN, DMITRY	
			ART UNIT	PAPER NUMBER
			2662	

DATE MAILED: 04/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/671,767	BLUM, SCOTT B.
	Examiner	Art Unit
	Dmitry Levitan	2662

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 29 March 2005.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-18 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-18 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

- Certified copies of the priority documents have been received.
- Certified copies of the priority documents have been received in Application No. _____.
- Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date _____.

5) Notice of Informal Patent Application (PTO-152)

6) Other: _____.

Amendment, filed 03/29/05, has been entered. Claims 1-18 remain pending.

Claim Rejections - 35 USC § 102

1. Claims 1, 7 and 13 are rejected under 35 U.S.C. 102(b) as being anticipated by Macera (US 5,490,252).

Macera teaches a method, machine-readable medium and apparatus of a mobile router (Broadband Enterprise switch on Fig. 1 and 3 and 4:5-27) comprising:

A first network interface card (one of network interface modules/cards 58 on Fig. 3 and 6:3-21) that couples to a first network (associated network from networks 68-76 on Fig. 3) and a second network interface card (one of network interface modules/cards 58 on Fig. 3 and 6:3-21) to couple to a second network (associated network from networks 68-76 on Fig. 3),

A system controller (maintenance and administration processor 64 on Fig. 3) that is coupled (bus 43 on Fig. 3 interconnecting electronic processing modules 60 6:3-6) to a processor (RISC processor of FPSE 5:51-67) and coupled to an Input/Output controller hub (router engine 66 on Fig. 3) that further couples to the first and second network interface cards (interfaces modules 58),

A memory subsystem (Fast path switching engine 62 on Fig. 3 and 6:3-21), having instructions for a network protocol independent bridge device driver (protocol independent bridge 1:65-67 and 2:1-21, inherently with bridge device driver, because the bridge interfaces networks, as shown on Fig. 3), which when executed by the processor, causes the system to bridge the first network and second network (route packets between networks on Fig. 3 and 6:30-48) by:

Interfacing the bridge device with a first and a second network interface card driver (inherently part of the system, because interface card drivers are essential for network interfaces),

Adaptively deriving topology of the first and second network from network packets received from the first and the second networks (using learned address table with knowledge of the location of other nodes on networks 27:5-37).

Delivering the received network packets based on MAC address contained in the received network packets and the derived topology (learning MAC addresses from the received packets 15:55-64 and using the learned address table for routing 27:5-37).

Claim Rejections - 35 USC § 103

2. Claims 2, 6, 8, 12, 14 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Macera in view of Machin (US 6,243,753).

Macera substantially teaches the limitations of claims 2, 6, 8, 12, 14 and 18.

Macera does not teach using standard driver/ Network Driver Interface Specification (NDIS).

Machin teaches using Network Driver Interface Specification (NDIS on 4:35-50). It would have been obvious to one of ordinary skill in the art at the time the invention was made to add using Network Driver Interface Specification (NDIS) of Machin to the system of Macera to incorporate standard driver, reducing the development complexity of the system.

3. Claims 3, 9 and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Macera in view of Machin in further view of Shannon (US 6,233,618).

Macera substantially teaches the limitations of claims 3, 9 and 15, including translating instructions from the protocol driver to effectuate transparency of the bridge device driver (forwarding packets directly from Home Agent to a Foreign agent 2:21-30).

Macera does not teach utilizing application program interfaces.

Shannon teaches utilizing application program interfaces (screening packets for information with Application Programming Interface 13:35-51).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to add utilizing application program interfaces of Shannon to the system of Macera to incorporate an effective screening tool to the system of Macera.

4. Claims 4, 5, 10, 11, 16 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Macera in view of Machin in further view of Hoare (US 4,627,052).

5. Regarding claims 4, 10 and 16, Macera teaches all the limitations of parent claims 1,7 and 13.

Macera does not teach generating and modifying entries of unmatched source addresses and associated network information of the received network packets in a distributed table.

Hoare teaches generating and modifying entries of unmatched source addresses and associated network information of the received network packets in a distributed table (entering the unmatched packets into a table 1:65-67 and 2:1-2).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to add generating and modifying entries of unmatched source addresses and associated network information of the received network packets in a distributed table of Hoare to the system of

Macera to incorporate an effective learning process to build an independent table to the system of Macera.

6. Regarding claims 5, 11 and 17, modified Macera teaches all the limitations of parent claims 4, 10 and 16.

Macera does not teach address filtering of the packets and delivering the packets according to their destination addresses, packet types and information in the distribution table.

Hoare teaches address filtering of the packets and delivering the packets according to their destination addresses, packet types and information in the distribution table (address filtering and forwarding the packets to the destination addresses after matching with a table 2:1-4 and 1:36-39).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to add address filtering of the packets and delivering the packets according to their destination addresses, packet types and information in the distribution table of Hoare to the system of Macera to improve efficiency of the system by reducing the packets transmitted in each network (1:40-44).

Response to Arguments

7. Applicant's arguments with respect to claims 1-18 have been considered but are moot in view of the new ground(s) of rejection.

Art Unit: 2662

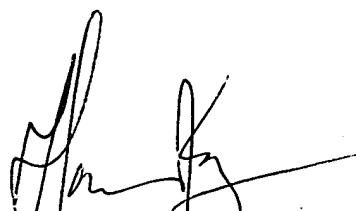
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dmitry Levitan whose telephone number is (571) 272-3093. The examiner can normally be reached on 8:30 to 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizou can be reached on (571) 272-3088. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Dmitry Levitan
Patent Examiner.
04/07/05



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